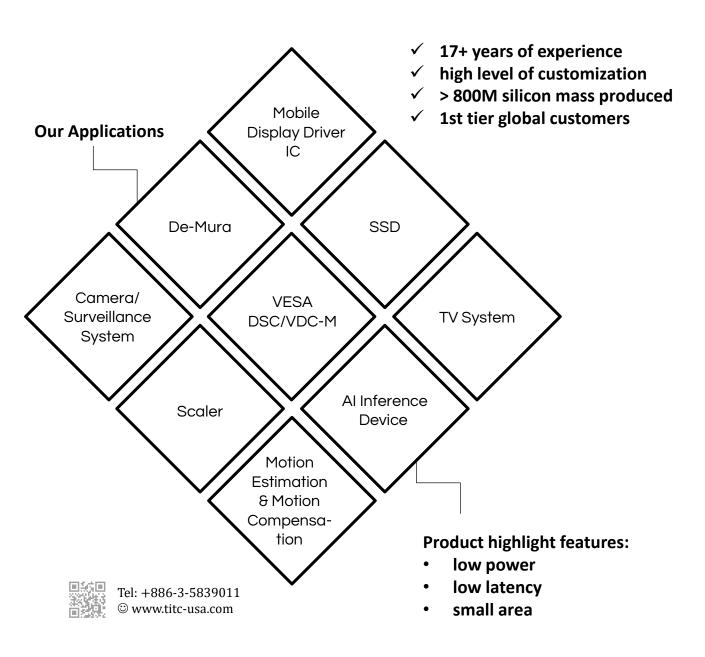


Image Compression IP specialist

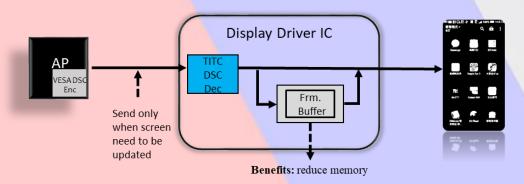




TITC VESA-DSC/VDCM Decoder IP

VESA DSC (Display Stream Compression) and VDC-M (VESA Display Stream Compression-M) are standard which is used for compressing and decompressing image display streams. It is designed for real-time systems, with real-time compression, transmission, decompression, and display. These standard IP could be used in many applications and save the transmission cost, such as between a mobile application processor and display panel, between a computer graphics and display monitor, and so on.

TITC provides a DSC decoder IP which is compatible to VESA DSC V1.1 and V1.2a, and VDC-M DEC IP is ready soon. Specially, TITC provides 6P/T versions DSC DEC, which could be used for 1 slice setting. These IP are configurable in display resolution (Up to 4K, UHD+, and 8K), bits per video component (8 and 10 bits), video output formats(RGB, YCbCr444, YUV422, and YUV420), and multiple slice per line setting (1, 2, or 4). TITC also provides customized service to shrink the IP area when no need to support the whole configuration.



> TITC VESA IP

Decoder IP	DSC v1.1 6P	DSC v1.2a 3P	DSC v1.2a 6P	VDCM v1.1.0 VDCM v1.2.2
Status	MP>1M	Ready	MP	Available soon
Throughput	6 pixel/clk	3 pixel/clk	6 pixel/clk @ RGB 3 pixel/clk @ YUV422	4 pixel/clk
Function Support	MMAP, BP, MPP, ICH	MMAP, BP, MPP, ICH	MMAP, BP, MPP, ICH	TX, BP, MPP, Fallbacks
	Color space Conversion	Color space Conversion	Color space Conversion	Color space Conversion
	Constant Bit Rate	Constant Bit Rate	Constant Bit Rate	Constant Bit Rate
	Multi-slice: 2 slice	Multi-slice: 2 slice/4 slice	Multi-slice: 2 slice/4 slice	Multi-slice: 2 slice/4 slice
Performance with 120MHz Clock	FHD@344+ FPS QHD@195+ FPS 4K@86+ FPS	FHD@172+ FPS QHD@97+ FPS 4K@43+ FPS	FHD@344+ FPS QHD@195+ FPS 4K@86+ FPS	FHD@228+ FPS QHD@130+ FPS 4K@57+ FPS
Source Format	RGB	RGB/YUV422/YUV420	RGB/YUV422	RGB/YUV422/YUV420
Bit Depth	8, 10 bit	8, 10 bit	8, 10 bit	8, 10 bit
Compression Ratio	8 bit: 1X~4X 10bit:1X~5X	8 bit: 1X~4X 10bit:1X~5X	8 bit: 1X~4X 10bit:1X~5X	8 bit: 1X~5X 10bit:1X~6X
Latency	5 clk	5 clk	5 clk	13 clk

Rm. 52-308, No.195, Sec. 4, Chung-Hsin Rd., Taiwan

Tel: +886-3-5839011 © www.titc-usa.com